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L17 and (line near3 driv\$3)	0

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<u>L18</u>	L17 and (line near3 driv\$3)	0	<u>L18</u>
<u>L17</u>	L16 and (plesiochronous\$2 or plesiosynchronous\$2 or (inherent near3 clock) or (without adj2 transmit\$4 adj2 clock))	17	<u>L17</u>
<u>L16</u>	(370/252,294,314,324).ccls.	2884	<u>L16</u>
<u>L15</u>	L14 and (line near3 driv\$3)	2	<u>L15</u>
<u>L14</u>	L13 and (plesiochronous\$2 or plesiosynchronous\$2 or (inherent near3 clock) or (without adj2 transmit\$4 adj2 clock))	8	<u>L14</u>
<u>L13</u>	(711/5,12,105,149,167).ccls.	3264	<u>L13</u>
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L12</u>	L11 and l10 and l8	0	<u>L12</u>
<u>L11</u>	L9 near8 (fixed near3 (portion or section or partition or part))	28	<u>L11</u>
<u>L10</u>	L9 near8 (vari\$4 near3 (portion or section or partition or part))	18	<u>L10</u>
<u>L9</u>	line near3 driv\$3	49300	<u>L9</u>
<u>L8</u>	(plesiochronous\$2 or plesiosynchronous\$2 or (inherent near3 clock) or (without adj2 transmit\$4 adj2 clock))	341	<u>L8</u>
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<u>L7</u>	l5 and l4	13	<u>L7</u>
<u>L6</u>	L5 and l4 and l2	13	<u>L6</u>






<u>L5</u>	L3 near8 (fixed near3 (portion or section or partition or part))	41	<u>L5</u>
<u>L4</u>	L3 near8 (vari\$4 near3 (portion or section or partition or part))	28	<u>L4</u>
<u>L3</u>	line near3 driv\$3	92846	<u>L3</u>
<u>L2</u>	(plesiochronous\$2 or plesiosynchronous\$2 or (inherent near3 clock) or (without adj2 transmit\$4 adj2 clock))	1264	<u>L2</u>
<u>L1</u>	(plesiochronous\$2 or plesiosynchronous\$2 or (inherent near3 clock) or (without adj2 transmit\$4 adj2 clock)) with ((in adj2 band) or (out adj2 band))	2	<u>L1</u>

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OPTION 1

Enter keywords or phrases, select fields, and select operators

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AND 	<input type="text"/>	In All Fields 
AND 	<input type="text"/>	In All Fields 

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OPTION 2

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<pre>(plesiochronous\$ or plesiosynchronous\$ or (inherent <near/3> clock) or (without <near/2> transmit\$ <near/2> clock)) <sentence> ((in <near/2> band) or (out <near/2> band))</pre>	
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» Note: You may use the search operators <and> or <or> without the start and end brackets <>.

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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL. IEE Journal or Magazine

IEEE CNF IEEE Conference
Proceeding

IEE CNF IEE Conference
Proceeding

IEEE STD IEEE Standard

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Terms used

plesiochronous or plesiosynchronous or inherent near/3 clock or without near/2 transmit near/2 clock sent

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1 [Fast detection of communication patterns in distributed executions](#)

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on C**

Full text available:  [pdf\(4.21 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [index](#)

Understanding distributed applications is a tedious and difficult task. Visualizations based on proc obtain a better understanding of the execution of the application. The visualization tool we use is University of Waterloo. However, these diagrams are often very complex and do not provide the l application. In our experience, such tools display repeated occurrences of non-trivial commun ...

2 [Probing the black box: User-level internet path diagnosis](#)

Ratul Mahajan, Neil Spring, David Wetherall, Thomas Anderson

October 2003 **Proceedings of the nineteenth ACM symposium on Operating systems princip**

Full text available:  [pdf\(403.57 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citing](#)

Diagnosing faults in the Internet is arduous and time-consuming, in part because the network is c across many administrative domains. We consider an extreme form of this problem: can end user pinpoint faults inside the network that degrade the performance of their applications? To answer l architecture for user-level Internet path diagnosis and a practical tool to diagnose paths in the ...

Keywords: measurement tools, path diagnosis

3 [Trunking of TDM and narrowband services over IP Networks](#)

James Aweya

January 2003 **International Journal of Network Management**, Volume 13 Issue 1

Full text available:  [pdf\(418.58 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citing](#)

The recent interest in IP as the vehicle for transporting TDM and narrowband services stems from transport network for voice, video, and data, and the flexibility with which new services can be in networks towards a 'broadband' IP-based environment is the 'graceful' interworking of the IP netw services, particularly with the circuit switched telephone network. A &l ...

4 [Columns: Risks to the public in computers and related systems](#)

Peter G. Neumann

January 2001 **ACM SIGSOFT Software Engineering Notes**, Volume 26 Issue 1

Full text available:  [pdf\(3.24 MB\)](#)

Additional Information: [full citation](#)

5 [The transport layer: tutorial and survey](#)

Sami Iren, Paul D. Amer, Phillip T. Conrad

December 1999 **ACM Computing Surveys (CSUR)**, Volume 31 Issue 4

Transport layer protocols provide for end-to-end communication between two or more hosts. This layer concepts and terminology, and a survey of transport layer services and protocols. The transport reference point, and compared and contrasted with nineteen other protocols designed over the past protocol features of twelve of the most important protocols are summarized in both text and table

Keywords: TCP/IP networks, congestion control, flow control, transport protocol, transport service

6 Exploring and exploiting wire-level pipelining in emerging technologies

Michael Thaddeus Niemier, Peter M. Kogge

May 2001

ACM SIGARCH Computer Architecture News , Proceedings of the 28th annual Computer architecture, Volume 29 Issue 2

Full text available: [pdf\(883.31 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citing:](#)

Pipelining is a technique that has long since been considered fundamental by computer architects. This paper is pushing the idea of pipelining to new and lower levels — particularly the device level. How this relates between their timing, architecture, and design will be studied in the context of an inherently self-synchronizing Quantum Cellular Automata (QCA). Results indicate that this nanotechnology offers t ...

7 Connection splitting: an efficient way of reducing call blocking in ATM

Subir K. Biswas, Rauf Izmailov, Bhaskar Sengupta

October 2000 **IEEE/ACM Transactions on Networking (TON)**, Volume 8 Issue 5

Full text available: [pdf\(271.49 KB\)](#)

Additional Information: [full citation](#), [references](#), [index terms](#)

Keywords: access control, asynchronous transfer mode, communication system routing, resource allocation

8 Distributed discrete-event simulation

Jayadev Misra

March 1986

ACM Computing Surveys (CSUR), Volume 18 Issue 1

Full text available: [pdf\(2.47 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citing:](#)

Traditional discrete-event simulations employ an inherently sequential algorithm. In practice, simulating this sequentiality, because only a modest number of events can be simulated. Distributed discrete-event simulation (a network of processors with asynchronous message-communicating capabilities) is proposed as an alternative to improve performance by partitioning the simulation among the component processors. The basic distributed

9 The effects of asymmetry on TCP performance

Hari Balakrishnan, Venkata N. Padmanabhan, Randy H. Katz

September 1997 **Proceedings of the 3rd annual ACM/IEEE international conference on Mobile Computing and Networking**

Full text available: [pdf\(2.02 MB\)](#)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

10 A Completely Integrated Low Jitter CMOS PLL for Analog Front Ends in Systems on Chip Embedded

Debapriya Sahu

January 2002

Proceedings of the 2002 conference on Asia South Pacific design automation

Full text available: [pdf\(202.10 KB\)](#) [Publisher Site](#)

Additional Information: [full citation](#), [abstract](#)

This paper describes the PLL designed for the analog front-end of the silicon tuner in the cable modulator. The PLL generates clocks (150-175MHz) for the DAC and hence the phase noise (jitter) requirement is very stringent (less than 1 degree of integrated phase error). Low noise design for all the main blocks was a key to achieving low reference spurs and supply/substrate injected spurs. The PLL uses two supplies. Charge pump and

11 Special issue: AI in engineering


D. Sriram, R. Joobhani

January 1985

ACM SIGART Bulletin, Issue 91

The papers in this special issue were compiled from responses to the announcement in the July 1995 notices posted over the ARPAnet. The interest being shown in this area is reflected in the sixty papers. About half the papers were received over the computer network.


- 12 Link and channel measurement: A simple mechanism for capturing and replaying wireless channel
Glenn Judd, Peter Steenkiste
August 2005 **Proceeding of the 2005 ACM SIGCOMM workshop on Experimental approaches to network analysis E-WIND '05**

Full text available:  [pdf\(6.06 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index](#)

Physical layer wireless network emulation has the potential to be a powerful experimental tool. An alternative to emulation, and traditional simulation, is to accurately model the wireless channel. In this paper we use real-world signal strength measurements to capture wireless channel traces. A key advantage of this approach is that with which these measurements can be obtained since virtually all wireless devices provide the raw data.

Keywords: channel capture, emulation, wireless

- 13 Physical interface: Fine-grained network time synchronization using reference broadcasts
Jeremy Elson, Lewis Girod, Deborah Estrin
December 2002 **ACM SIGOPS Operating Systems Review**, Volume 36 Issue S1

Full text available:  [pdf\(2.10 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citing papers](#)

Recent advances in miniaturization and low-cost, low-power design have led to active research in low-power sensors and actuators. Time synchronization is critical in sensor networks for diverse purposes such as coordinated actuation, and power-efficient duty cycling. Though the clock accuracy and precision of traditional distributed systems, strict energy constraints limit the resources available ...

- 14 Virtual time
David R. Jefferson
July 1985 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 7 Issue 3

Full text available:  [pdf\(1.82 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citing papers](#)


Virtual time is a new paradigm for organizing and synchronizing distributed systems which can be used for discrete event simulation and distributed database concurrency control. Virtual time provides a flexible abstraction of real time the same way that virtual memory provides an abstraction of real memory. It is implemented using a time synchronization protocol distinguished by its reliance on lookahead-rollback, a ...

- 15 Measurement and analysis of the error characteristics of an in-building wireless network
David Eckhardt, Peter Steenkiste
August 1996 **ACM SIGCOMM Computer Communication Review , Conference proceedings on Computer architectures, and protocols for computer communications**, Volume 26 Issue 4

Full text available:  [pdf\(168.08 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citing papers](#)

There is general belief that networks based on wireless technologies have much higher error rates than wired technologies such as optical fiber, coaxial cable, or twisted pair wiring. This difference has motivated research specifically for wireless networks. While the error characteristics of wired networks have been well characterized and available for wireless LANs. In this paper we report the results of a study characterizing ...

- 16 Illustrative risks to the public in the use of computer systems and related technology
Peter G. Neumann
January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1

Full text available:  [pdf\(2.54 MB\)](#)Additional Information: [full citation](#)

- 17 Pen computing: a technology overview and a vision
André Meyer
July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3

Full text available:  [pdf\(5.14 MB\)](#)Additional Information: [full citation](#), [abstract](#), [citing papers](#), [index terms](#)


This work gives an overview of a new technology that is attracting growing interest in public as well. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction, picking up the familiar pen and paper interface metaphor. From this follows a set of conclusions into context with other emerging technologies and visions. Starting with a short historic ...

18 System architectures for computer music

John W. Gordon

June 1985

ACM Computing Surveys (CSUR), Volume 17 Issue 2

Full text available:  [pdf\(4.61 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citing:](#)


Computer music is a relatively new field. While a large proportion of the public is aware of computer music, there seems to be a need for a better understanding of its capabilities and limitations in terms of synthesis and hardware. This article addresses that need by surveying and discussing the architecture of existing systems. Requirements vary according to what the system will be used for. Common uses for computer music are ...

19 S-connect: from networks of workstations to supercomputer performance

Andreas G. Nowatzky, Michael C. Browne, Edmund J. Kelly, Michael Parkin

May 1995

ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual conference on Computer architecture, Volume 23 Issue 2

Full text available:  [pdf\(1.38 MB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citing:](#)

S-Connect is a new high speed, scalable interconnect system that has been developed to support shared computing resources. It uses off-the-shelf CMOS technology to directly drive fiber-optic systems and can realize bisection bandwidths comparable to high-end MPP systems while being >10x more efficient. It does not rely on centralized switches, but rather is composed of adaptive, topology independent ...

20 Mixed-signal design and simulation: A 16-bit mixed-signal microsystem with integrated CMOS

Robert M. Senger, Eric D. Marsman, Michael S. McCorquodale, Fadi H. Gebara, Keith L. Kraver, Matt

June 2003

Proceedings of the 40th conference on Design automation

Full text available:  [pdf\(793.60 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citing:](#)

In this work, we report on an unprecedented design where digital, analog, and MEMS technologies are integrated on a single-chip CMOS microsystem. The convergence of these technologies has enabled the design of a microinstrument ideally suited for controlling environmental and bio-implantable sensors.

Keywords: ADC, MEMS, PGA, SD, SoC, clock generation, design methodology, inductor, low power microsystem, mixed-signal, system-on-chip, varactor

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